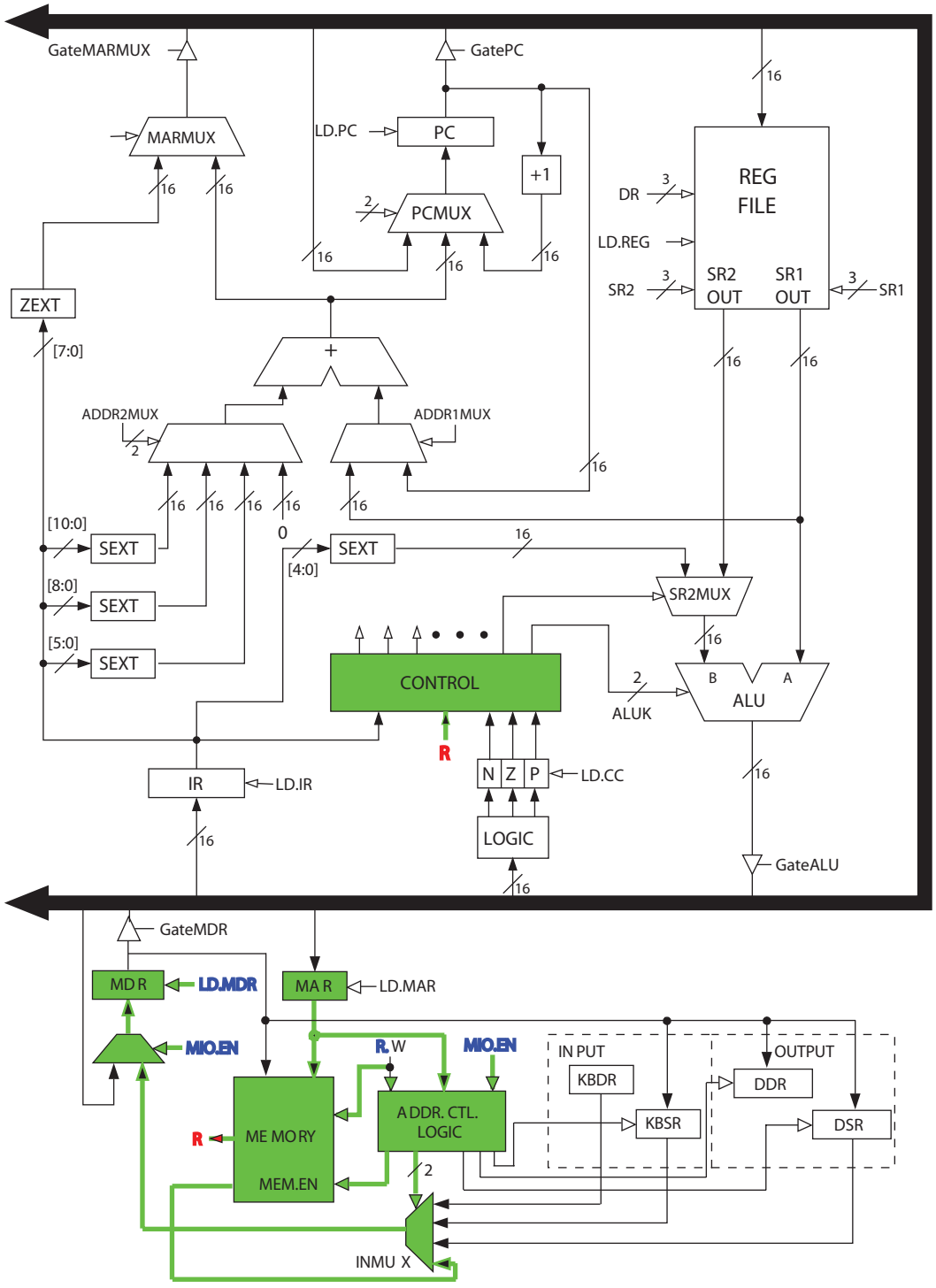

GateMARMUX
GatePC
16
MARMUX
LD.PC
PC
+1
REG
FILE
DR
3
LD.REG
SR2
SR1
OUT
OUT
3
SR1
2
PCMUX
16
16
16
ZEXT
[7:0]
+
ADDR2MUX
ADDR1MUX
2
16
16
16
16
16
16
16
[10:0]
SEXT
0
SEXT
16
[4:0]
[8:0]
SEXT
SR2MUX
[5:0]
SEXT
CONTROL
2
ALUK
B
A
ALU
R
IR
LD.IR
N
Z
P
LD.CC
LOGIC
16
16
GateALU
GateMDR
MD R
LD.MDR
MA R
LD.MAR
MIO.EN
R. W
MIO.EN
IN PUT
KBDR
OUTPUT
DDR
R
ME MO RY
MEM.EN
A DDR. CTL.
LOGIC
KBSR
DSR
2
INMU X